

DSG-NPS R&D Meeting Minutes

Date: February 09, 2021

Time: 11:00AM – 12:15 PM

Attendees: Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Pablo Campero, Brian Eng, George Jacobs, Mindy Leffel, Tyler Lemon, Marc McMullen, and Amrit Yegneswaran

1. CSS screen development status

1.1. Aaron Brown will continue development of an additional confirm button for the PMT Settings screen

1.1.1. Currently the voltage and current set limit parameters have to be confirmed separately. This new button would allow user to confirm settings for both simultaneously

1.2. Aaron Brown will generate all future CSS-BOY screens via Python script

2. Peter Bonneau and Aaron Brown are developing a block diagram to show each component of Hardware Interlock System; Mary Ann Antonioli will generate a Visio drawing

3. Procurement

3.1. Remaining fifty 15-pin SAMTEC connectors have arrived, totaling 100 each of 15- and 8-pin connectors

3.2. The 20 Ohmic Instruments humidity sensors have arrived; they will be tested to determine which will work best for Hardware Interlock System

3.2.1. Brian Eng will set up Keysight system in DSG control room

4. Cable fabrication/testing

4.1. Mindy Leffel completed two Radial-to-SAMTEC-connectors HV cables

4.1.1. One cable has a 20 AWG grounding wire soldered to the braided shield; all future cables will have this grounding wire

4.2. HV board will be shipped from IPN Orsay for testing Radial-to-SAMTEC-connectors HV cables

4.3. Marc McMullen has two fully populated (twenty-four 2-M Ω resistors each) PCBs that can be used in test chassis

4.4. Marc McMullen will develop an AutoCAD schematic to show internal connections (resistors and safety loop) of test chassis